

FIG. 1

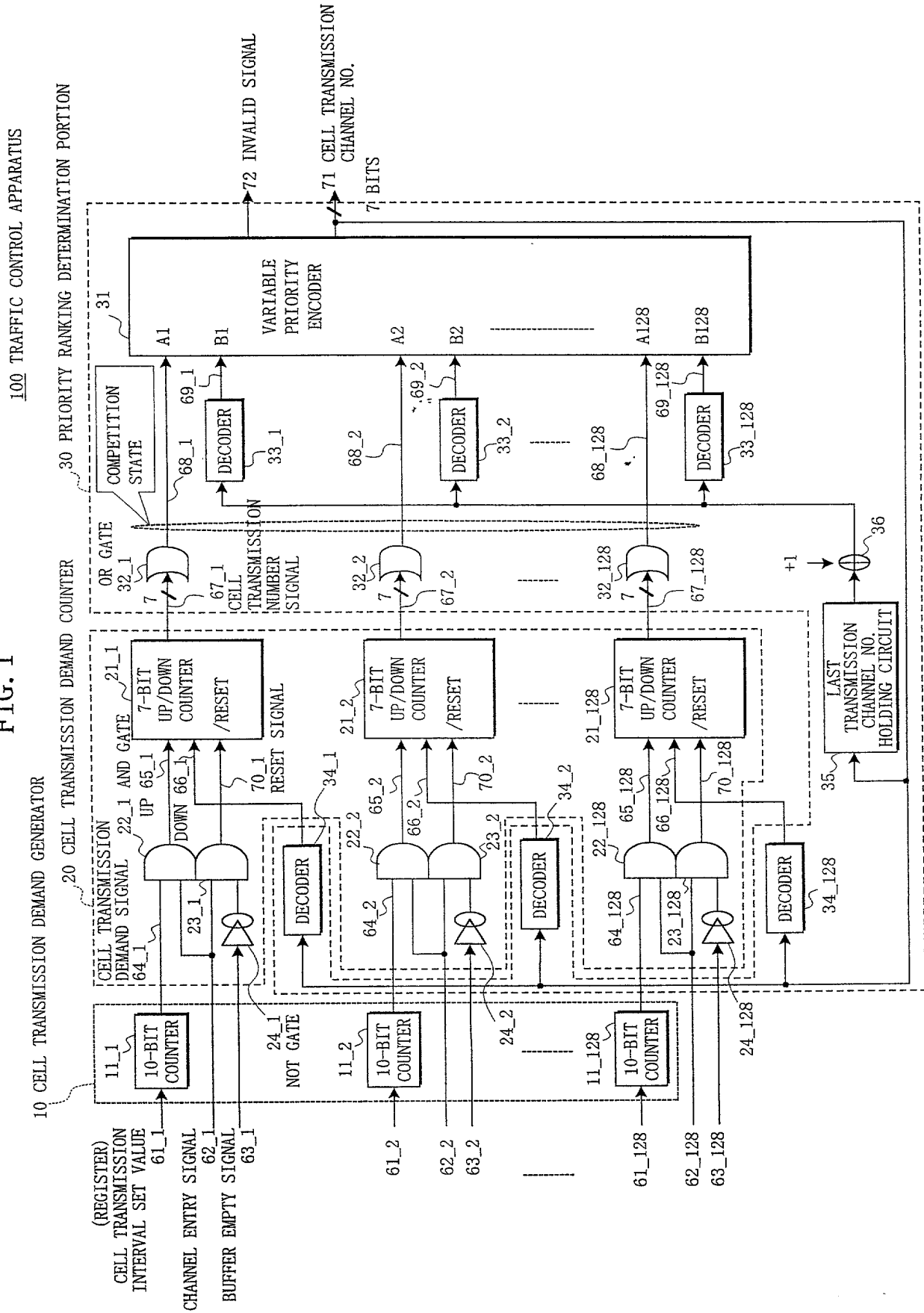
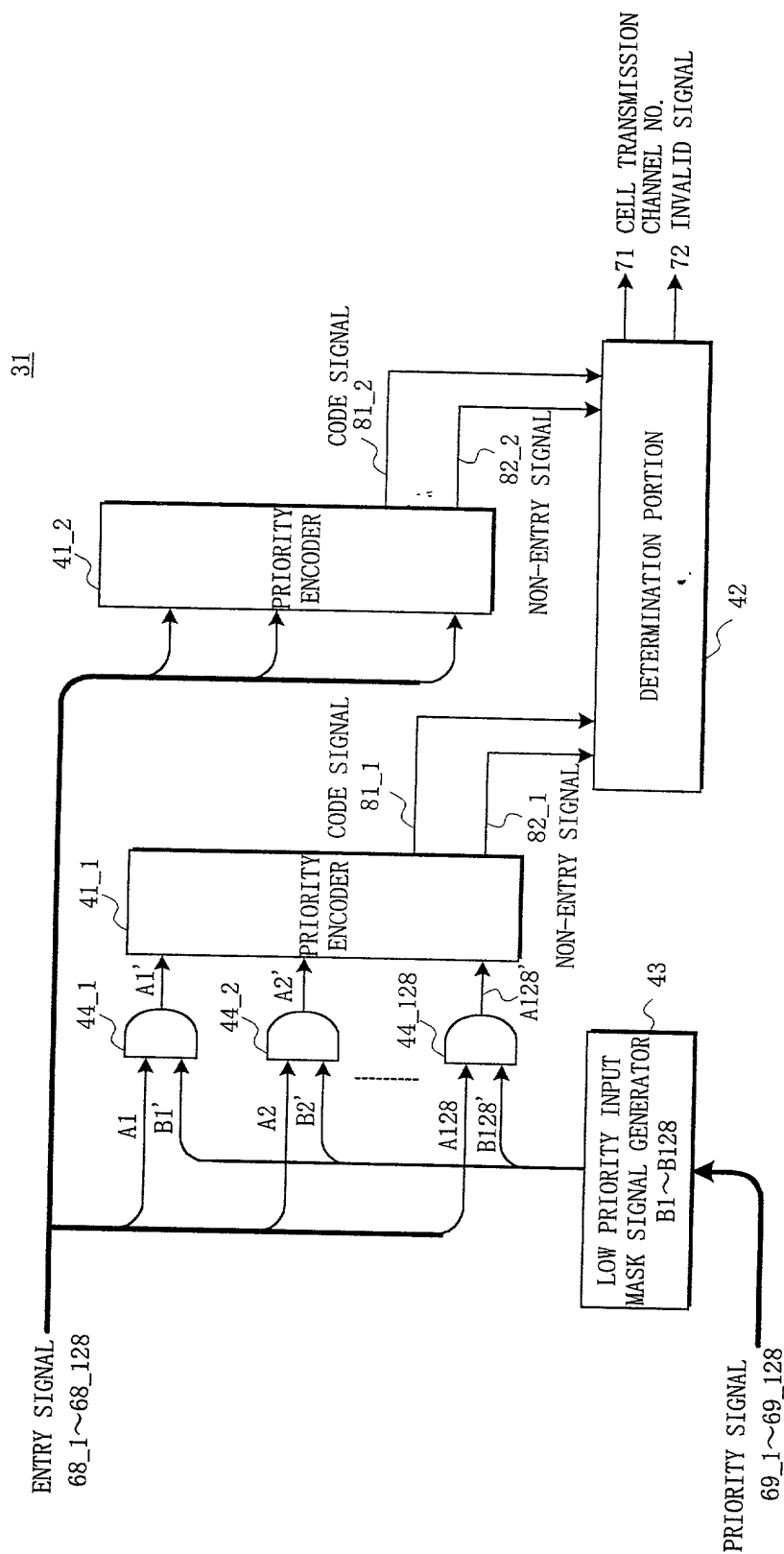


FIG. 2



OUTPUT CODE OF ADDITION CIRCUIT 36=M

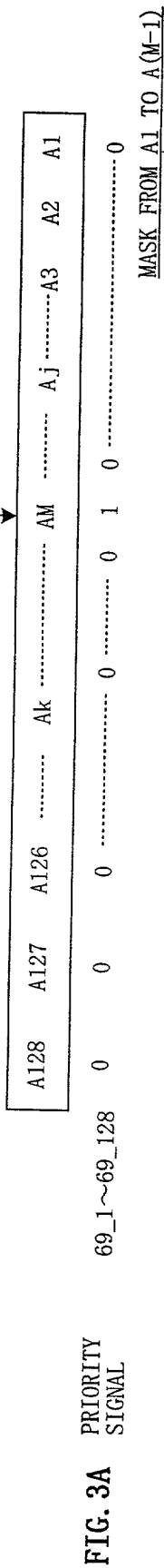


FIG. 3A



FIG. 3B



FIG. 3C

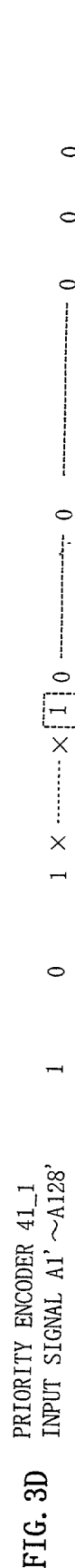


FIG. 3D



FIG. 3E



FIG. 3F



FIG. 3G



FIG. 3H

FIG. 4

INPUT SIGNAL				OUTPUT SIGNAL	
CODE SIGNAL 81_1	NON-ENTRY SIGNAL 82_1	CODE SIGNAL 81_2	NON-ENTRY SIGNAL 82_2	CHANNEL NO. 71	INVALID SIGNAL 72
×	INVALID	×	INVALID	×	INVALID
k	VALID	×	INVALID	k	VALID
×	INVALID	j	VALID	j	VALID
k	VALID	j	VALID	k	VALID

× :DON' T CARE

FIG. 6

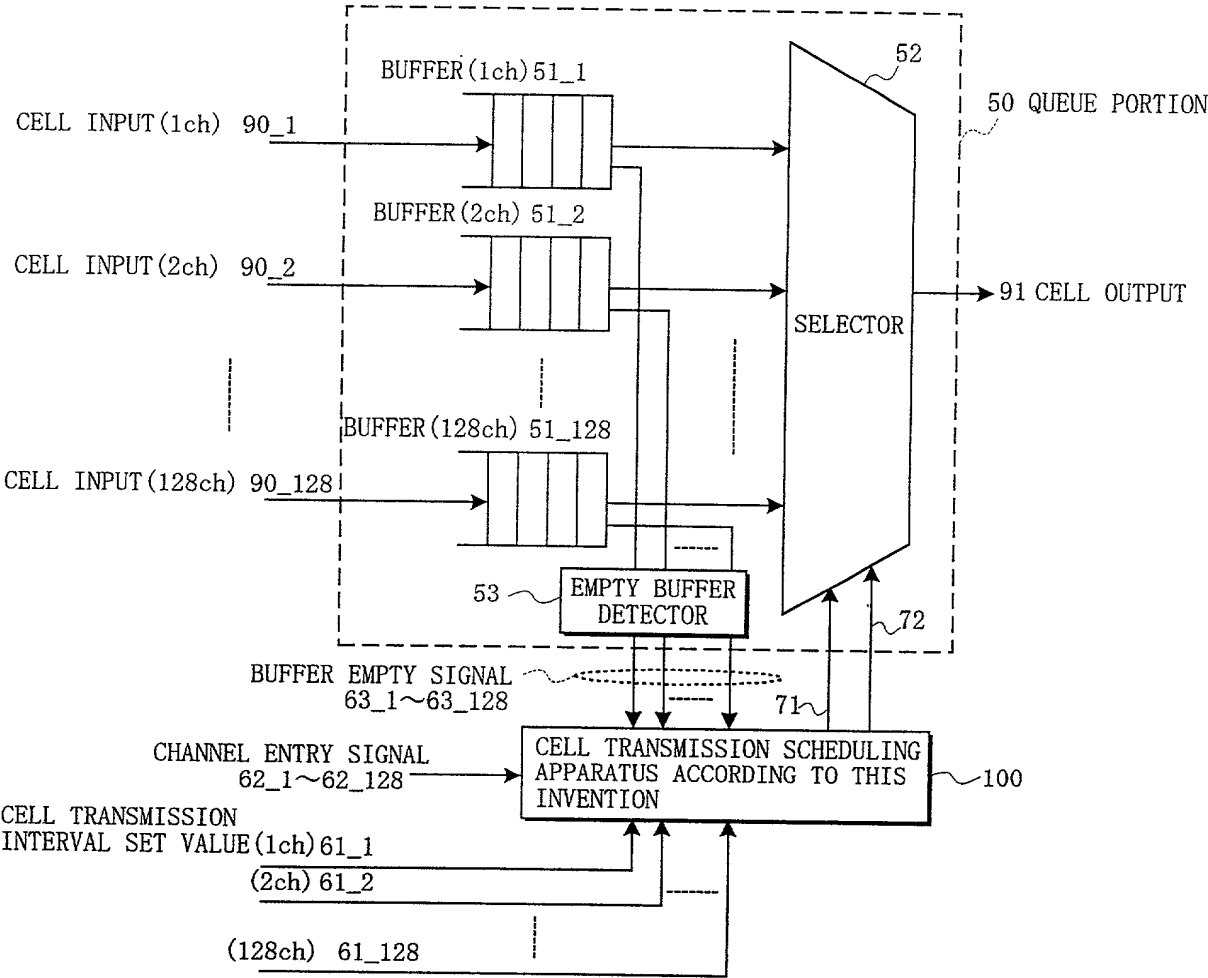


FIG. 5

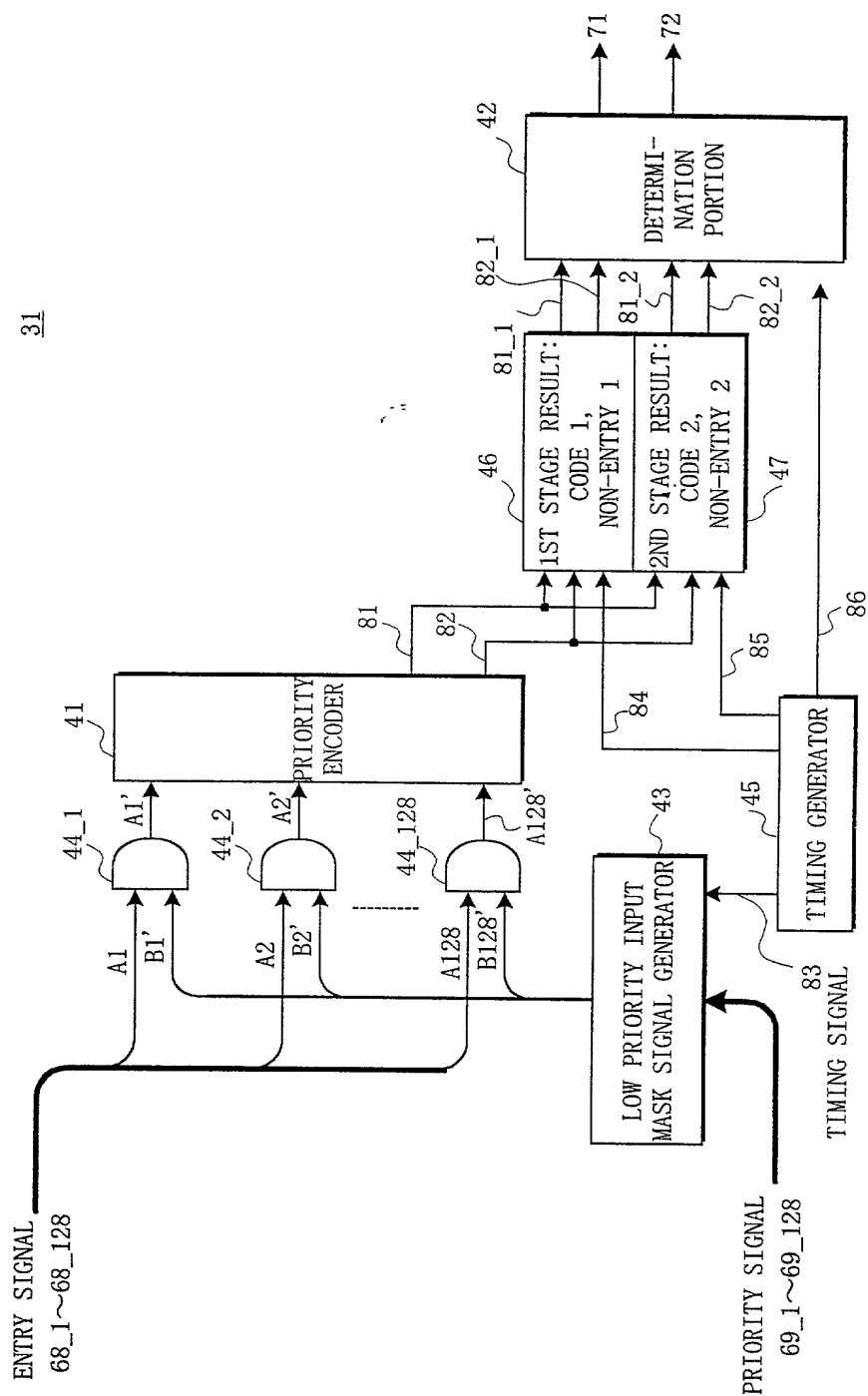


FIG. 7A " 03/23/60

CASE OF CHANNEL NUMBER N=4

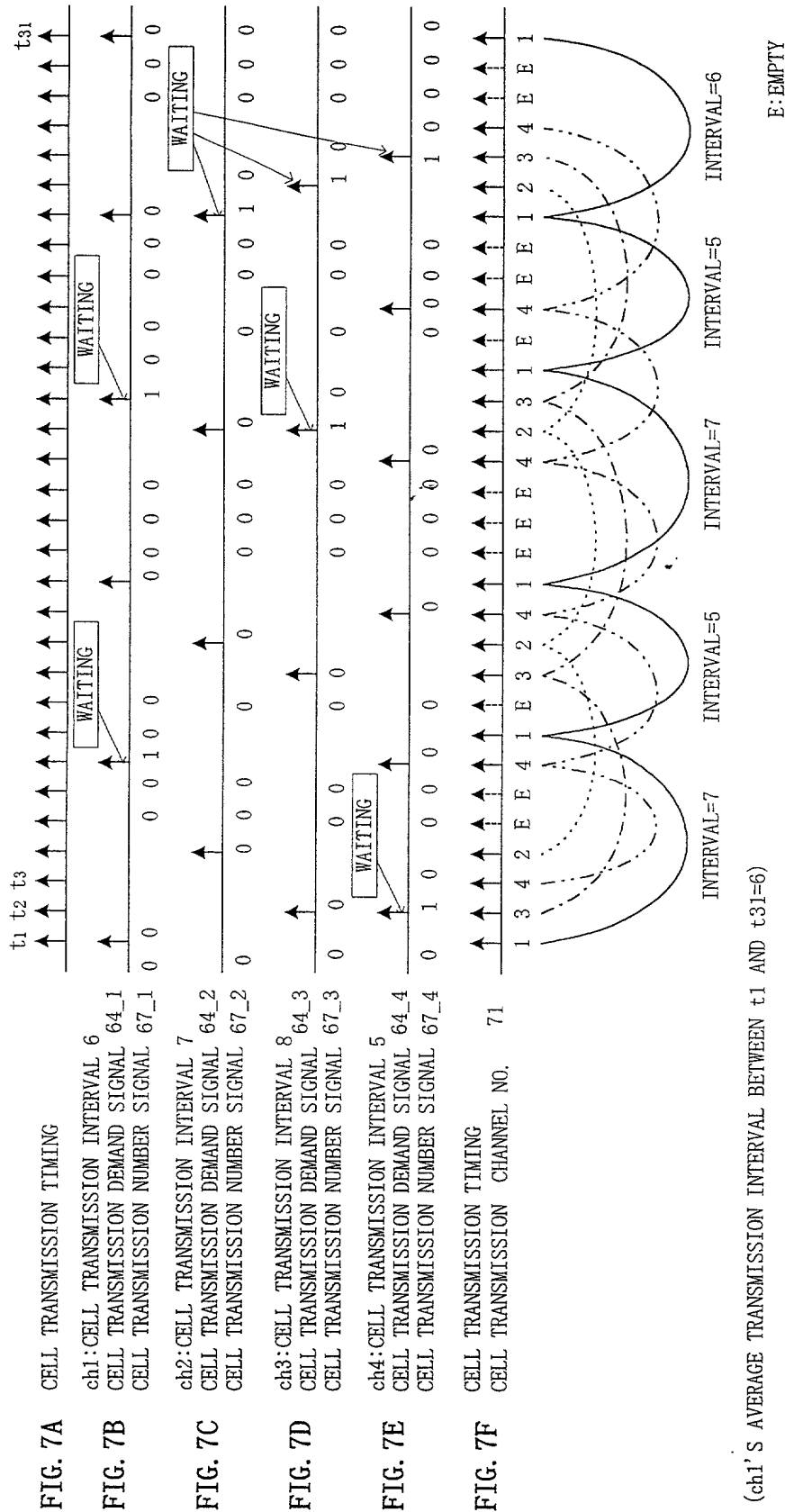


FIG. 8A CELL TRANSMISSION TIMING

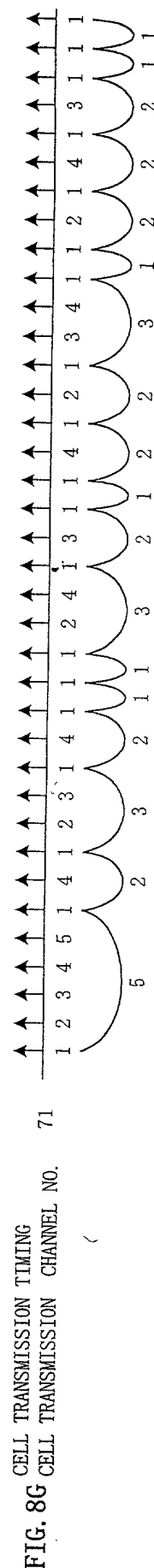
ch1: CELL TRANSMISSION INTERVAL 2
CELL TRANSMISSION DEMAND SIGNAL 64_1
CELL TRANSMISSION NUMBER SIGNAL 67_1

ch2: CELL TRANSMISSION INTERVAL 7
CELL TRANSMISSION DEMAND SIGNAL 64_2
CELL TRANSMISSION NUMBER SIGNAL 67_2

ch3: CELL TRANSMISSION INTERVAL 8
CELL TRANSMISSION DEMAND SIGNAL 64_3
CELL TRANSMISSION NUMBER SIGNAL 67_3

ch4: CELL TRANSMISSION INTERVAL 5
CELL TRANSMISSION DEMAND SIGNAL 64_4
CELL TRANSMISSION NUMBER SIGNAL 67_4

ch5: CELL TRANSMISSION INTERVAL 40
CELL TRANSMISSION DEMAND SIGNAL 64_5
CELL TRANSMISSION NUMBER SIGNAL 67_5



ch1' S CELL TRANSMISSION INTERVAL
(AVERAGE TRANSMISSION INTERVAL BETWEEN t1 AND t37=2)